MODULE DESCRIPTOR

Module Title

Electronics 2

Reference	EN2510	Version	7
Created	January 2023	SCQF Level	SCQF 8
Approved	March 2004	SCQF Points	15
Amended	August 2023	ECTS Points	7.5

Aims of Module

To provide the student with the knowledge and skills to enable analysis, synthesis and design of basic standard circuit configurations of analogue and digital circuits, and the use of hardware description languages.

Learning Outcomes for Module

On completion of this module, students are expected to be able to:

1	Analyse standard digital and analogue circuits using appropriate engineering principles
2	Create sequential logic circuits to solve broadly-defined problems
3	Report upon the design, simulation, and application of standard analogue circuits

Indicative Module Content

Data conversion, Introduction to ADC and DAC circuits. Identification, analysis and elimination of circuit hazards. MSI circuits, including encoders, decoders, multiplexers, demultiplexers, applications. Sequential circuits, flip-flop types, characteristic equations, synchronous counters, shift registers; flow diagram representation of digital systems. Introduction to basic concepts and use of, Algorithmic State Machines; applications in sequential circuits. Introduction to programmable devices. Introduction to VHDL coding of digital systems. Simple transistor (low-frequency) model. Analysis and synthesis of feedback circuits, two-transistor feedback amplifiers. Non-ideal operational amplifier, typical operational amplifier circuit configurations and applications, eg amplifiers, filters, oscillators.

Module Delivery

This is a lecture based course supplemented with tutorial sessions, laboratory exercises and student centred learning.

Module Ref:	EN2510 v7

Indicative Student Workload	Full Time	Part Time
Contact Hours	50	50
Non-Contact Hours	100	100
Placement/Work-Based Learning Experience [Notional] Hours	N/A	N/A
TOTAL	150	150
Actual Placement hours for professional, statutory or regulatory body		

ASSESSMENT PLAN

If a major/minor model is used and box is ticked, % weightings below are indicative only.

Component 1

Type:	Coursework	Weighting:	50%	Outcomes Assessed:	2, 3
Description:	Laboratory investigation.				

Component 2

Type:	Examination	Weighting:	50%	Outcomes Assessed:	1
Description:	Closed book examination.				

MODULE PERFORMANCE DESCRIPTOR

Explanatory Text

The module has 2 components and to gain an overall pass a minimum D grade must be achieved in each component. The component weighting is as follows: C1 is worth 50% and C2 is worth 50%.

Examination:

	Coursework: A	B	C	D	E	F	NS
A	A	A	B	B	E	E	
B	A	B	B	C	E	E	
C	B	B	C	C	E	E	
D	B	C	C	D	E	E	
E	E	E	E	E	E	F	
F	E	E	E	E	F	F	

NS	Non-submission of work by published deadline or non-attendance for examination

Module Requirements

Prerequisites for Module	EN1512 Introduction to Analogue Electronics and Signals or equivalent and EN1541 Introduction to Digital Electronics and Engineering Programming or equivalent.
Corequisites for module	None.
Precluded Modules	None.

INDICATIVE BIBLIOGRAPHY

1. BEARDS, P., 2002. Analog and Digital Electronics: A First Course. 2nd ed. Upper Saddle River, NJ: Prentice Hall.
2. FLOYD, T., 2015. Digital Fundamentals. 11th ed. Upper Saddle River, NJ: Prentice Hall.
3. KATZ, R. and BORRIELLO, G., 2005. Contemporary Logic Design. 2nd ed. Upper Saddle River, NJ: Prentice Hall.
4. ROTH, C.H., 2013. Fundamentals of Logic Design. 7th ed. Boston, MA: Thomson Brooks/Cole.