MODULE DESCRIPTOR

Module Title

Electronics 2

Reference	EN2510	Version	4
Created	May 2017	SCQF Level	SCQF 8
Approved	March 2004	SCQF Points	15
Amended	May 2017	ECTS Points	7.5

Aims of Module

To provide the student with the knowledge and skills to enable analysis, synthesis and design of basic standard circuit configurations of analogue and digital circuits, and the use of hardware description languages.

Learning Outcomes for Module

On completion of this module, students are expected to be able to:

1	Analyse and design standard digital circuits.
2	Analyse and design standard analogue circuits.
3	Design and simulate standard digital circuits.
4	Design and simulate standard analogue circuits.

Indicative Module Content

Data conversion, Introduction to ADC and DAC circuits. Identification, analysis and elimination of circuit hazards. MSI circuits, including encoders, decoders, multiplexers, demultiplexers, applications. Sequential circuits, flip-flop types, characteristic equations, synchronous counters, shift registers; flow diagram representation of digital systems. Introduction to basic concepts and use of, Algorithmic State Machines; applications in sequential circuits. Introduction to programmable devices. Introduction to VHDL coding of digital systems. Simple transistor (low-frequency) model. Analysis and synthesis of feedback circuits, two-transistor feedback amplifiers. Non-ideal operational amplifier, typical operational amplifier circuit configurations and applications, eg amplifiers, filters, oscillators.

Module Delivery

This is a lecture based course supplemented with tutorial sessions, laboratory exercises and student centred learning.

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Indicative Student Workload	Full Time	Part Time
Contact Hours	48	48
Non-Contact Hours	102	102
Placement/Work-Based Learning Experience [Notional] Hours	N/A	N/A
TOTAL	150	150
Actual Placement hours for professional, statutory or regulatory body		

ASSESSMENT PLAN

If a major/minor model is used and box is ticked, % weightings below are indicative only.

Component 1

Type:	Coursework	Weighting:	30%	Outcomes Assessed:	3, 4
Description:	Based on laboratory investigations on circuit design/simulations; the result of which will be presented in an appropriate medium.				

Component 2

Type:	Examination	Weighting:	70%	Outcomes Assessed:	1, 2
Description:	Closed book examination.				

MODULE PERFORMANCE DESCRIPTOR

Explanatory Text

To pass the module, you must achieve a 40% weighted average mark from the exam and coursework. In addition you need to achieve at least 35% in both the individual exam and coursework components.

Module Grade	Minimum Requirements to achieve Module Grade:
A	=>70%
B	60-69%
C	50-59%
D	40-49%
E	35-39%
F	0-34%
NS	Non-submission of work by published deadline or non-attendance for examination

Module Requirements

Prerequisites for Module	Introduction to Electronics (EN1510) or the equivalent.
Corequisites for module	None.
Precluded Modules	None.

INDICATIVE BIBLIOGRAPHY

1. BEARDS, P., 2002. Analog and Digital Electronics: A First Course. 2nd ed. Upper Saddle River, NJ: Prentice Hall.
2. FLOYD, T., 2015. Digital Fundamentals. 11th ed. Upper Saddle River, NJ: Prentice Hall.
3. KATZ, R. and BORRIELLO, G., 2005. Contemporary Logic Design. 2nd ed. Upper Saddle River, NJ: Prentice Hall.
4. ROTH, C.H., 2013. Fundamentals of Logic Design. 7th ed. Boston, MA: Thomson Brooks/Cole.